

Connecting Width and Structure in Knowledge Compilation

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What is Knowledge Compilation?

- You have a **task**
 - Boolean SAT (is there a satisfying assignment?)

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What is Knowledge Compilation?

- You have a **task**
 - Boolean SAT (is there a satisfying assignment?)
 - #SAT (model counting) (how many satisfying assignments?)
 - probabilistic evaluation
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- **Idea:** **compile** the input into a format that is *designed* to solve efficiently your task

Why would I do that?

- Without knowledge compilation

Input class \mathcal{C}_1 $\xrightarrow{\text{Algo. 1}}$ Result

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Compilation target
for your task

Generic algo.
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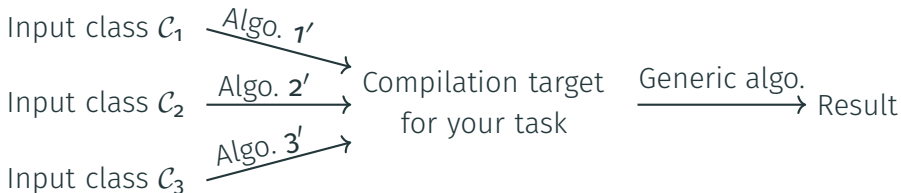
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- With knowledge compilation: **modularity!**



Studying the compilation targets

- Tradeoffs between:
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→ When can we convert from one target to another?

- We are interested in **#SAT** and **probability evaluation**

Target classes in knowledge compilation

For #SAT and probabilistic evaluation, two main restrictions on compilation targets:

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Question: what are the links between the two?

- Circuit C of **treewidth** $\leq k$ $\xrightarrow{O(|C| \times \exp(k))}$ **d-SDNNF**

Plan

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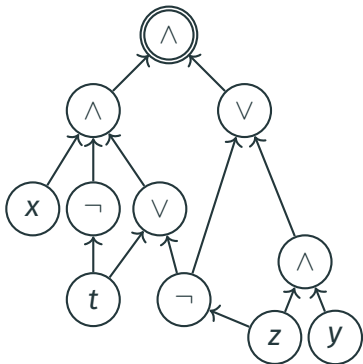
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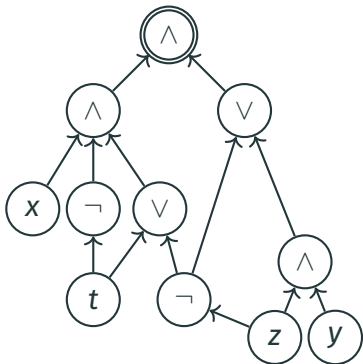
- Application to provenance and probabilistic databases

Treewidth and d-SDNNFs

Bounded treewidth Boolean circuits

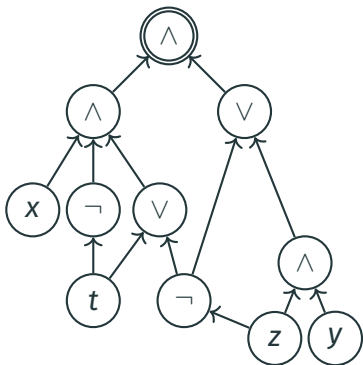


Bounded treewidth Boolean circuits



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Bounded treewidth Boolean circuits



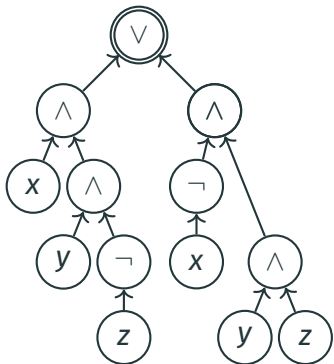
Treewidth of C = that of the underlying graph

We can do **message passing**:

Theorem (Lauritzen & Spielgelhalter, 1988)

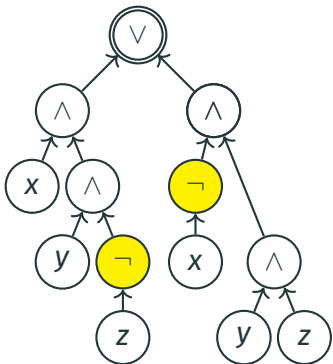
Fix $k \in \mathbb{N}$. Given a Boolean circuit C of **treewidth** $\leq k$, we can compute its **probability** in time $O(f(k) \times |C|)$, where f is singly exponential

d-SDNNF

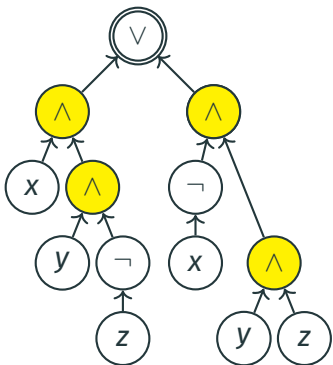


d-SDNNF

- **Negation Normal Form:** negations only applied to the leaves

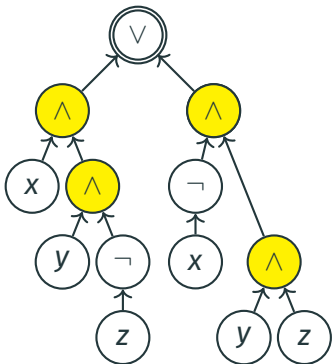


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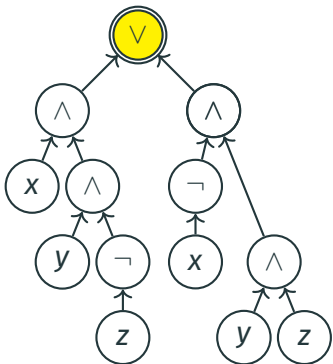
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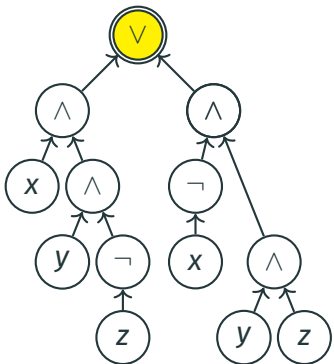
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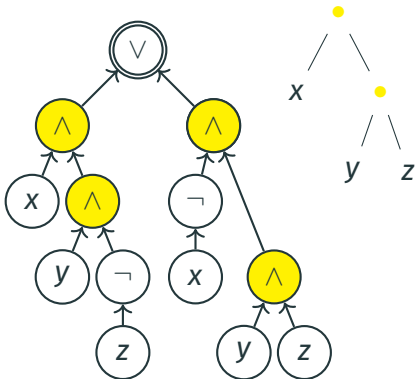
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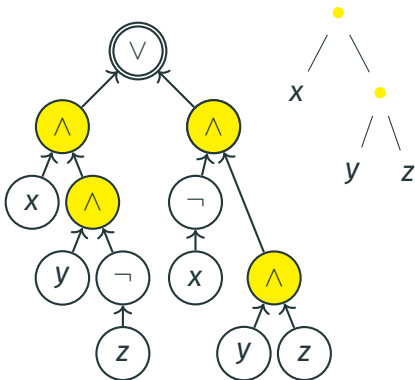
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- **Structured**: there is a **vtree** that structures the \wedge -gates

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Treewidth and d-SDNNFs: Upper bound

Theorem (Bova & Szeider, 2017)

Let C be a Boolean circuit on m variables of **treewidth** $\leq k$.

There exists a **d-SDNNF** equivalent to C of size $O(m \times g(k))$,
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Let C be a Boolean circuit of **treewidth** $\leq k$.

We can compute a **d-SDNNF** equivalent to C in time $O(|C| \times f(k))$,
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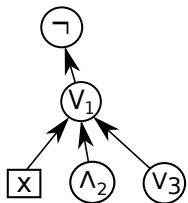
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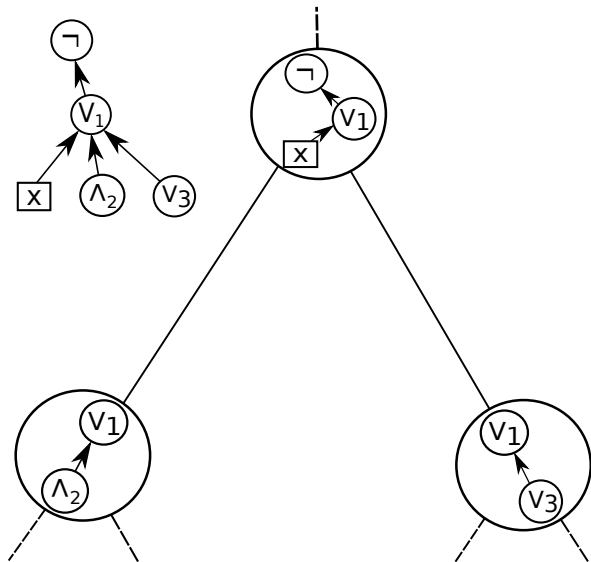
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Applications: recapturing message passing, and enumeration of satisfying valuations

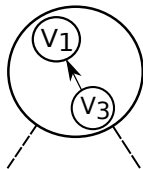
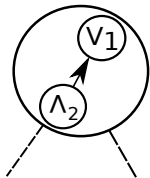
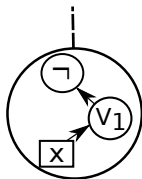
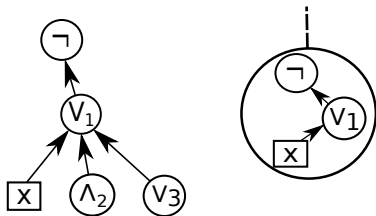
Construction sketch



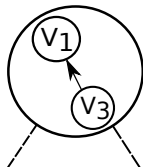
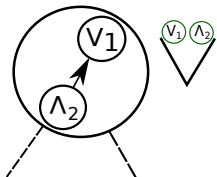
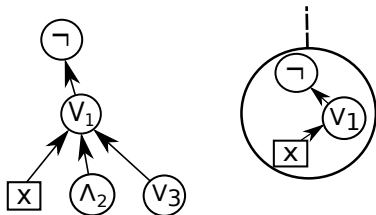
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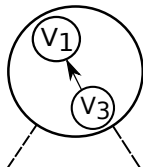
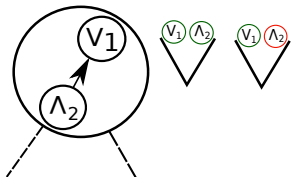
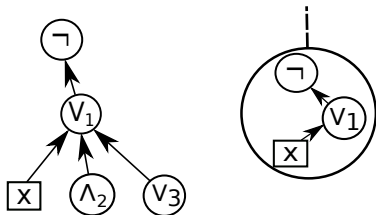
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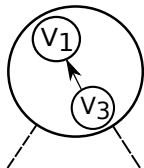
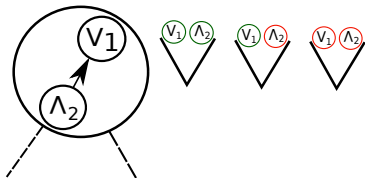
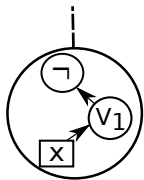
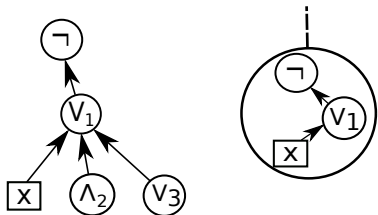
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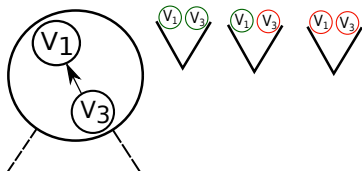
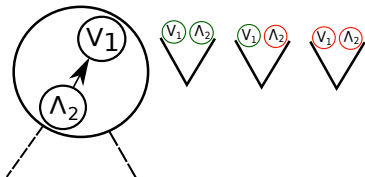
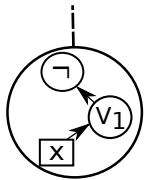
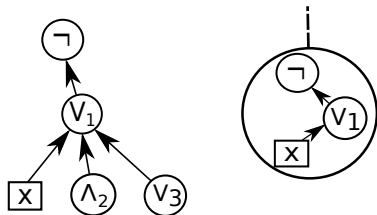
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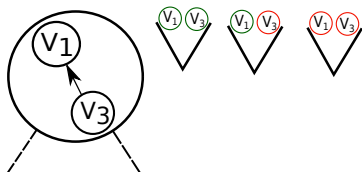
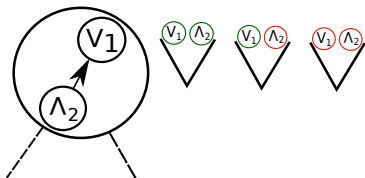
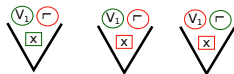
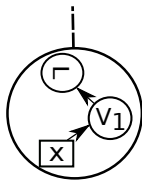
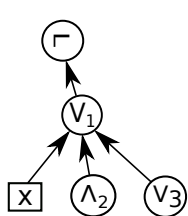
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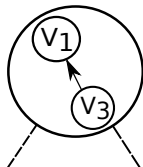
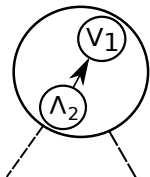
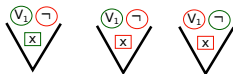
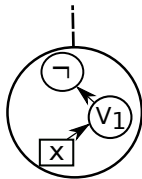
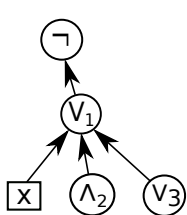
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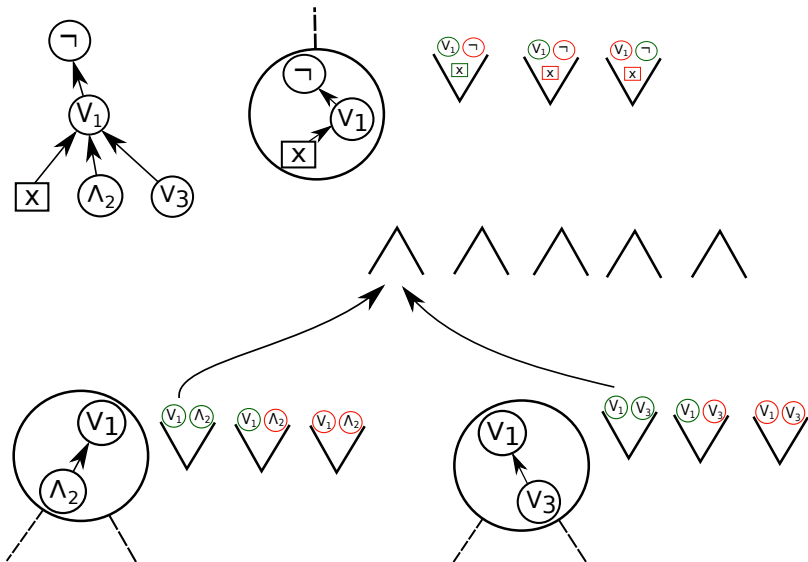
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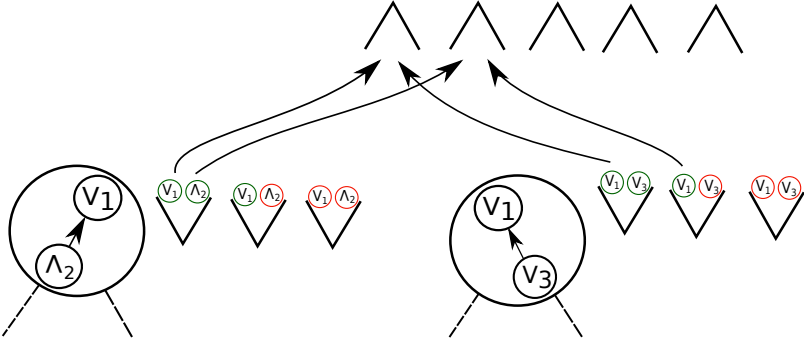
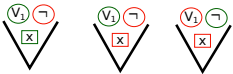
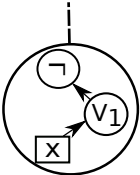
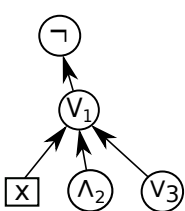
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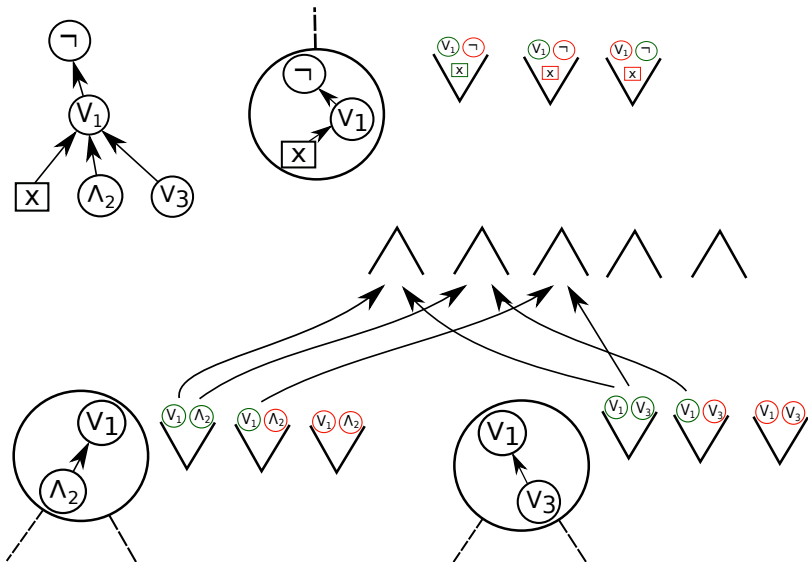
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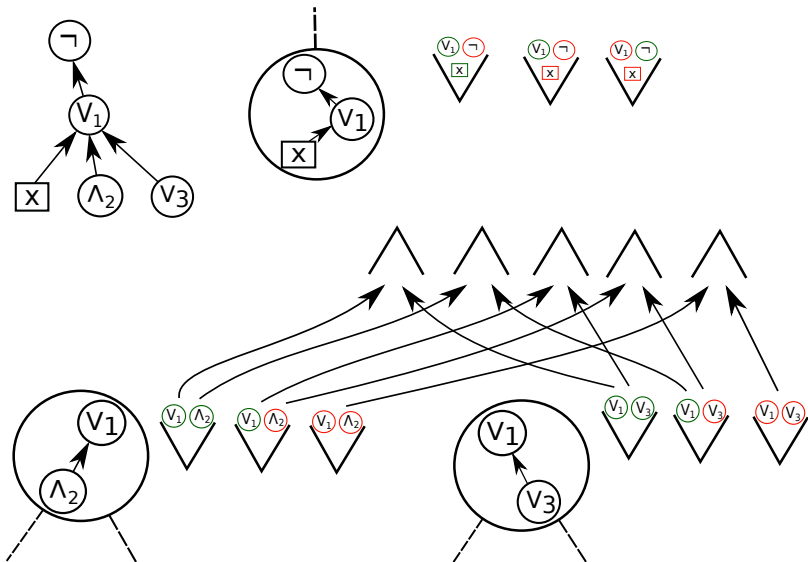
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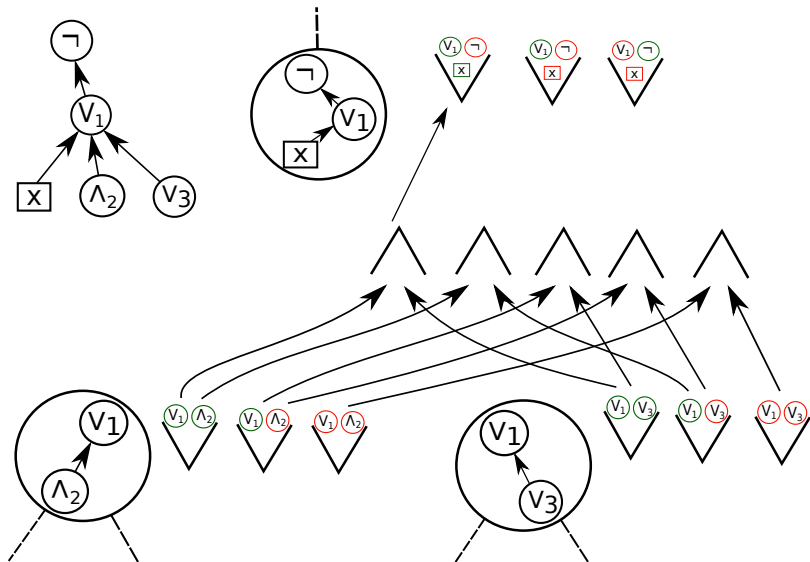
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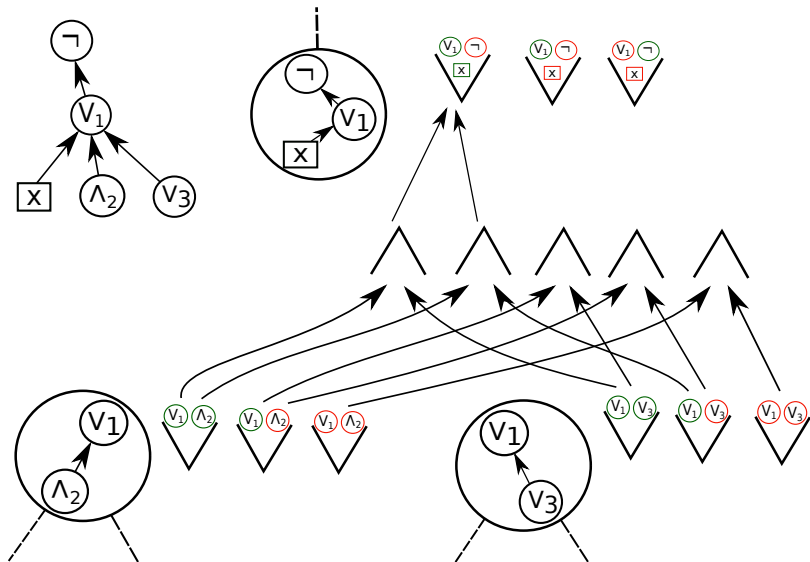
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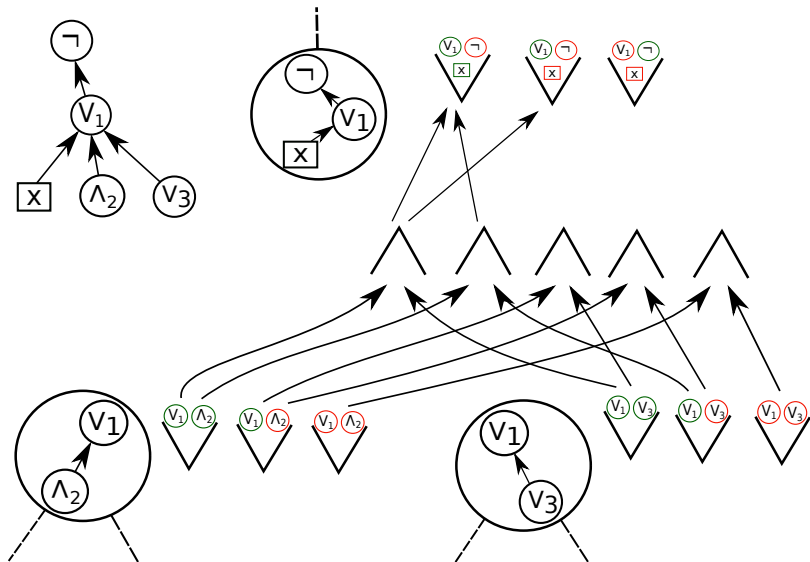
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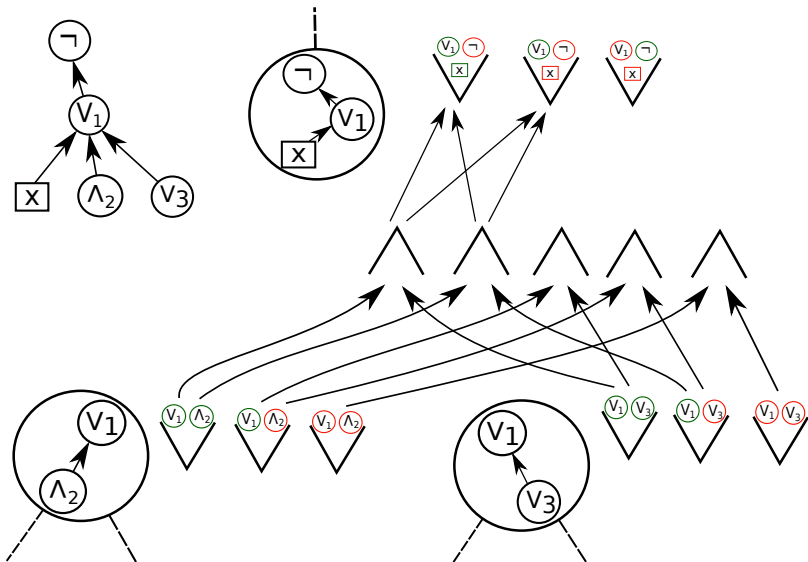
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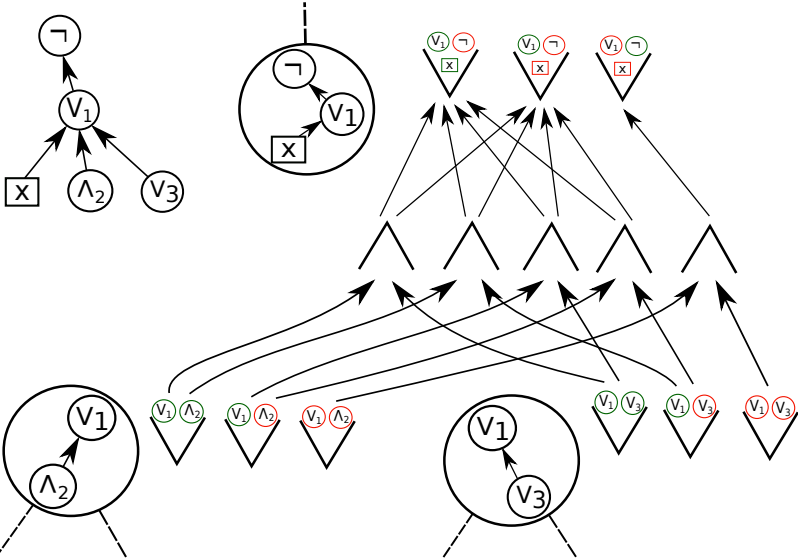
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Treewidth and d-SDNNFs: Lower bound

- Already applies to very restricted Boolean circuits: **monotone DNFs and CNFs**

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Let φ be a **monotone DNF** of **treewidth k** , let $a := \text{arity}(\varphi)$ and $d := \text{degree}(\varphi)$. Then any **d -SDNNF** for φ has size $\geq 2^{\lfloor \frac{k}{3 \times a^3 \times d^2} \rfloor} - 1$

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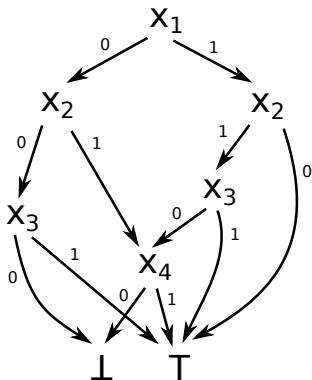
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Pathwidth and OBDDs

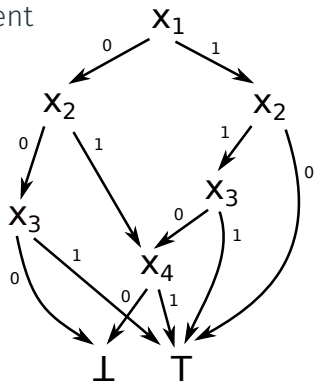
Ordered Binary Decision Diagrams (OBDDs)

- **DAG** with sink nodes $\{\top, \perp\}$ and internal nodes labeled by variables



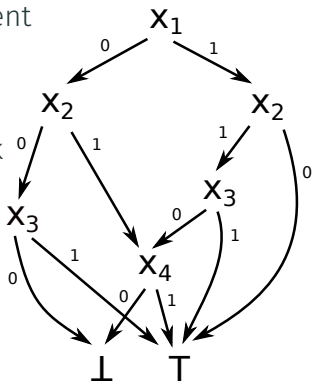
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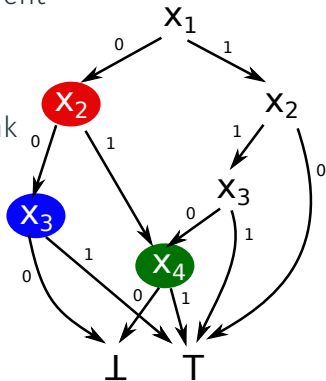
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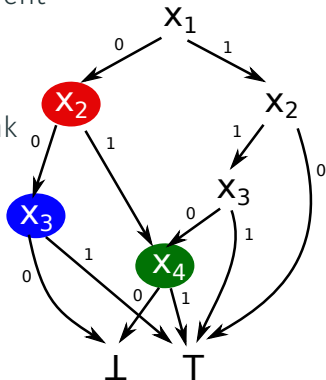
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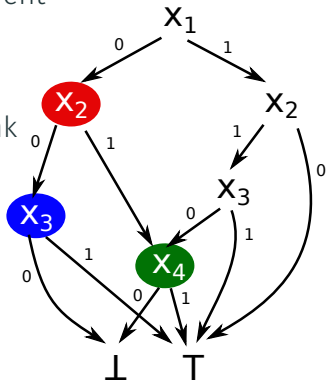


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- **Width** of the OBDD \simeq largest number of nodes that are labeled by the same variable



Pathwidth and OBDDs: Upper and lower bounds

Upper bound:

Theorem (Bova & Slivovsky, 2017)

Let φ be a CNF or DNF of **pathwidth k** . We can compile φ into an **OBDD** of width 2^{k+2} (hence of size $\leq nb_vars \times 2^{k+2}$)

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- Again, this is a **generic** lower bound!
- For monotone DNF/CNF φ of constant arity and degree, the smallest width of an OBDD for φ is $2^{\Theta(\text{pathwidth}(\varphi))}$

Application to **provenance**

Provenance: definition

Definition

The *provenance* $\text{Prov}(q, I)$ of query q on relational instance I is the Boolean function with facts of I as variables and such that for any valuation $\nu : I \rightarrow \{0, 1\}$, $\text{Prov}(q, I)$ evaluates to \top under ν iff

$$\{F \in I \mid \nu(F) = 1\} \models q$$

Example: Provenance

$$\exists x y z (R(x, y) \wedge S(y, z)) \vee (S(x, y) \wedge R(y, z))$$

R

b c

c a

c d

S

a b

d b

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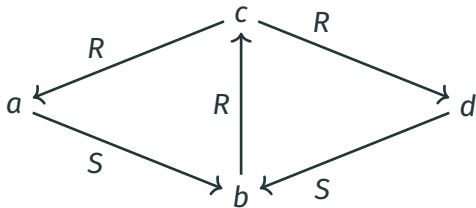
c a

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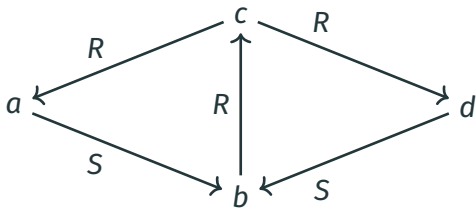
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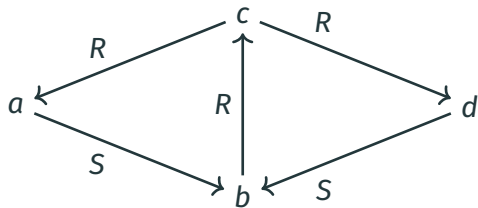
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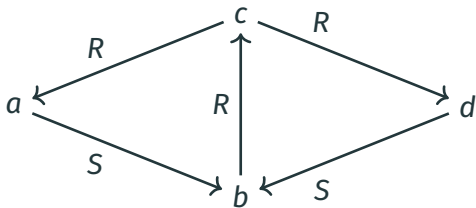
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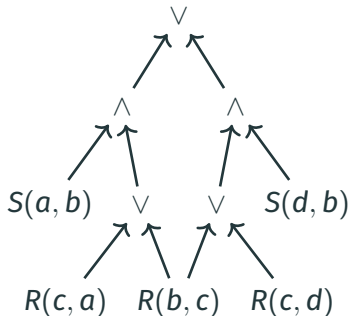
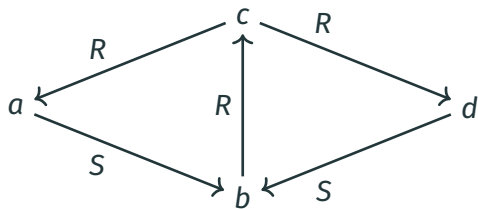
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- Hence, there are queries for which d-SDNNF representations of the lineage have size exponential in the **treewidth** of the database!

Theorem (Our contribution)

There is a constant $d \in \mathbb{N}$ such that the following is true. Let σ be an arity-2 signature, and Q a connected UCQ $^\neq$ which is **intricate** on σ . For any instance I on σ of **treewidth** k , any **d -SDNNF** representing the lineage of Q on I has size $2^{\Omega(k^{1/d})}$

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- Future work:
 - Get rid of arity and degree assumptions?
 - Notion of width for d-SDNNFs?
 - Lower bound for d-DNNFs?

Thanks for your attention!